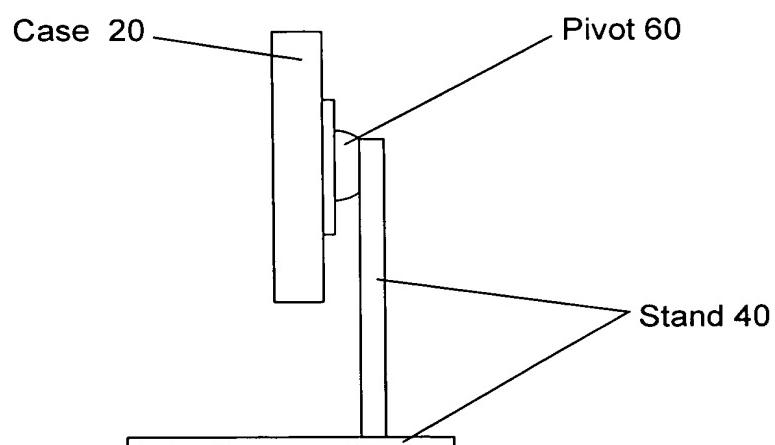
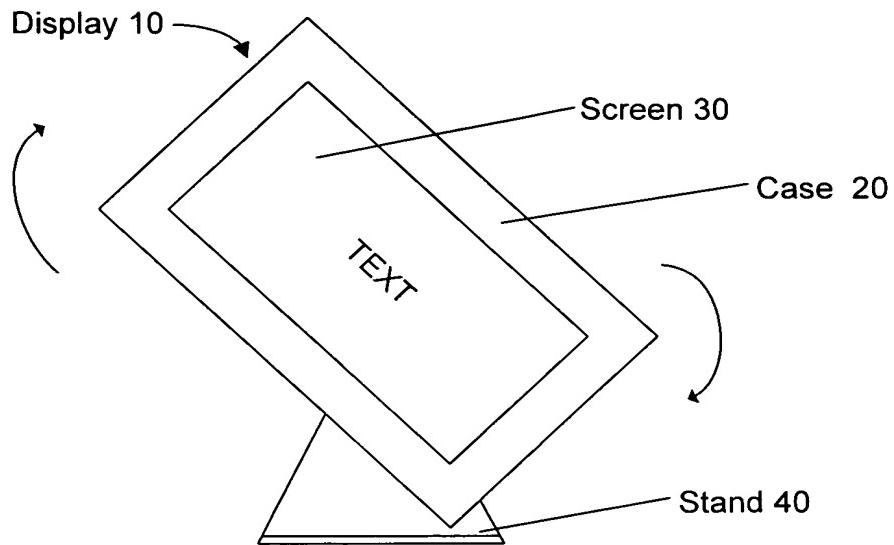


**Figure 1A**

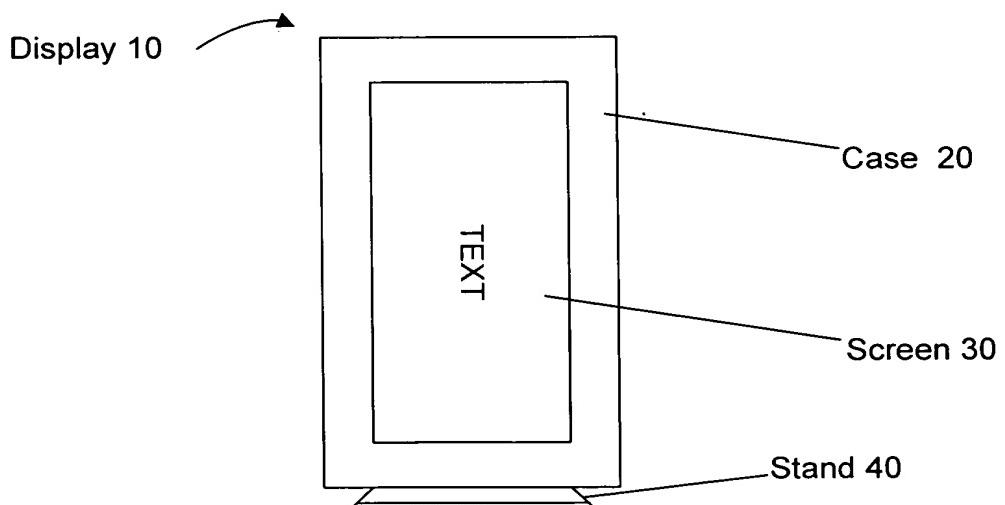


**Figure 1B**



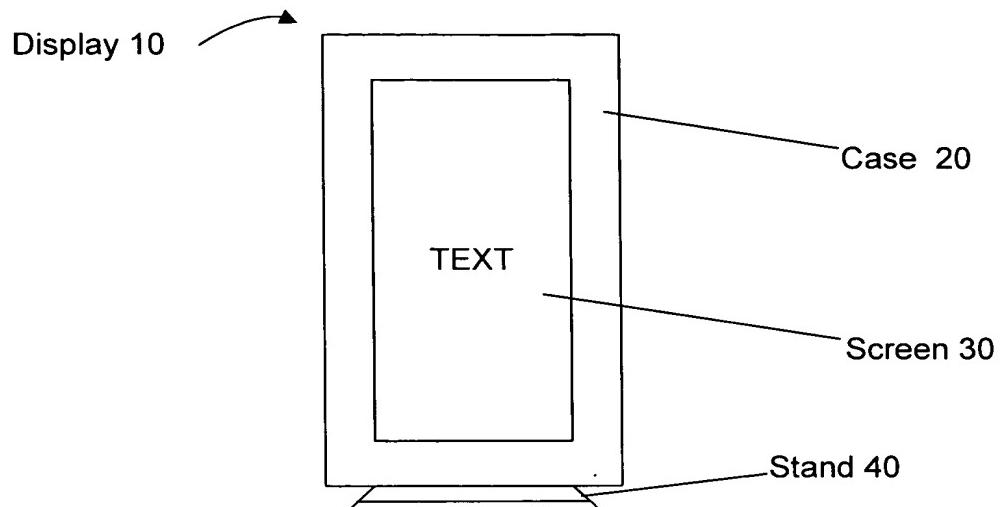


**Figure 1C**

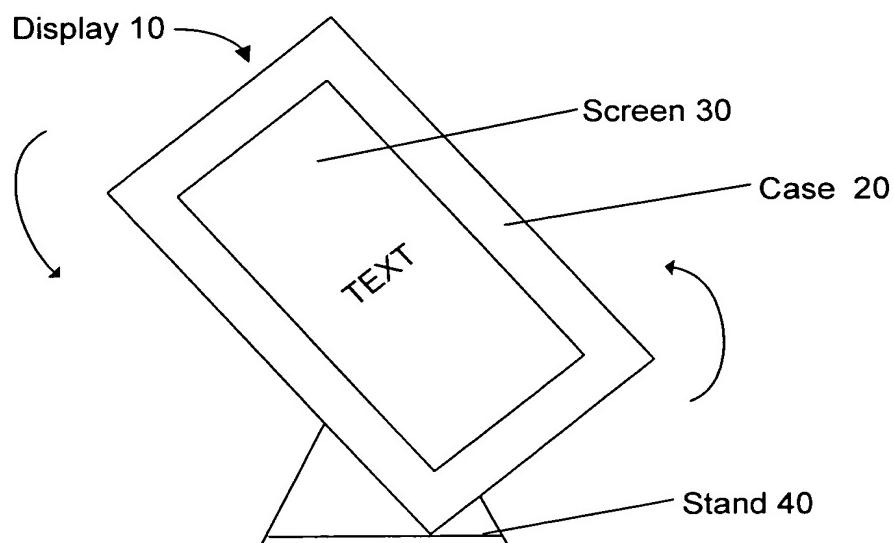


**Figure 1D**



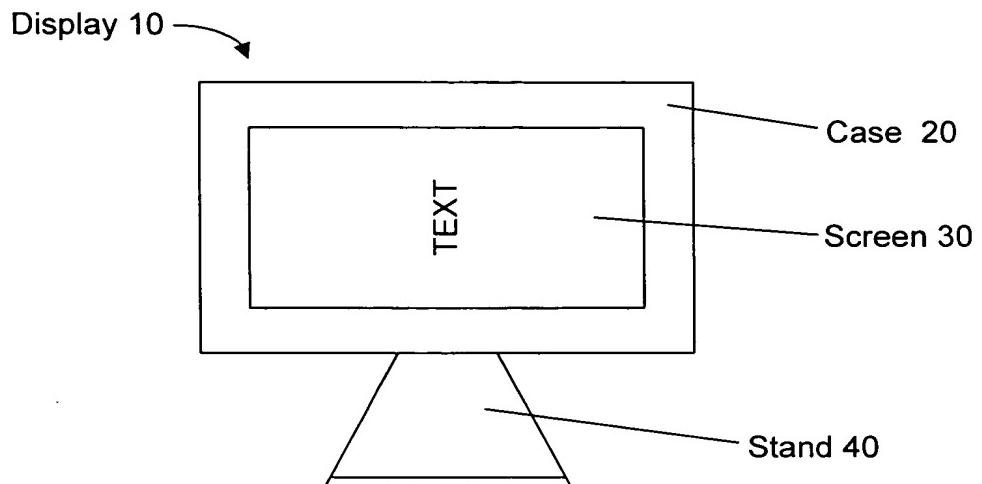
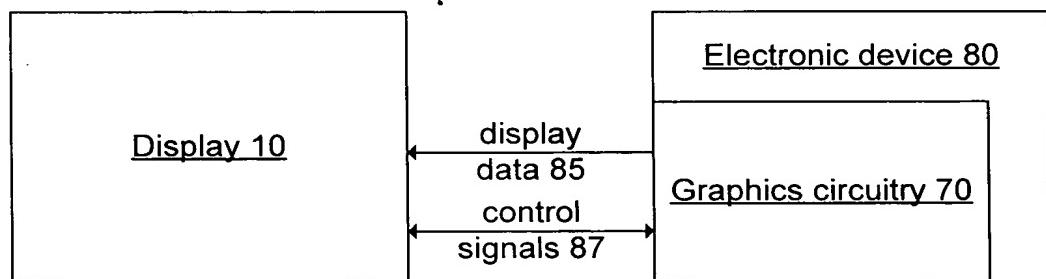
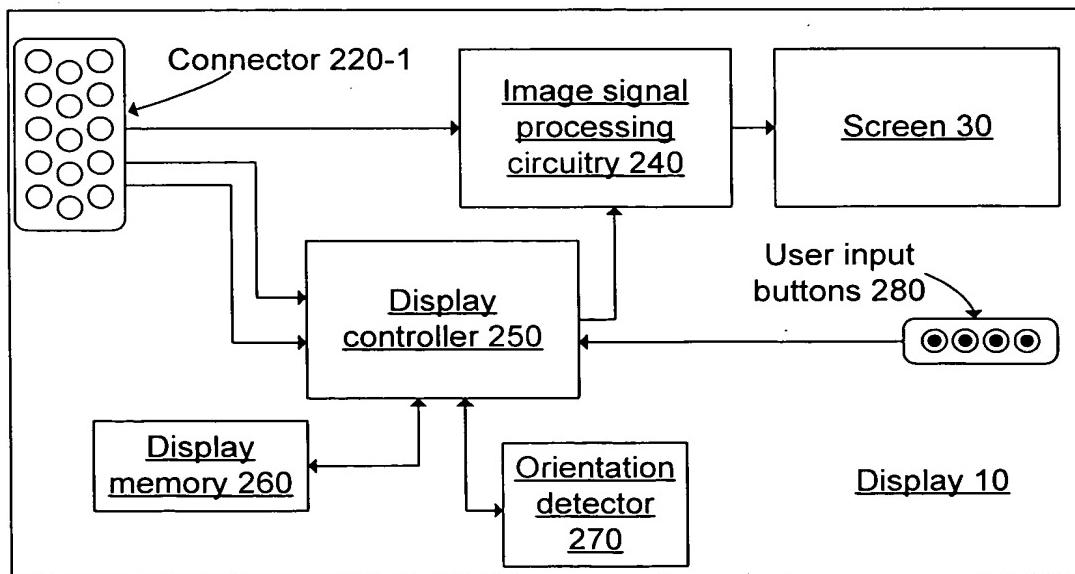


**Figure 1E**



**Figure 1F**



**Figure 1G****Figure 1H****Figure 2**

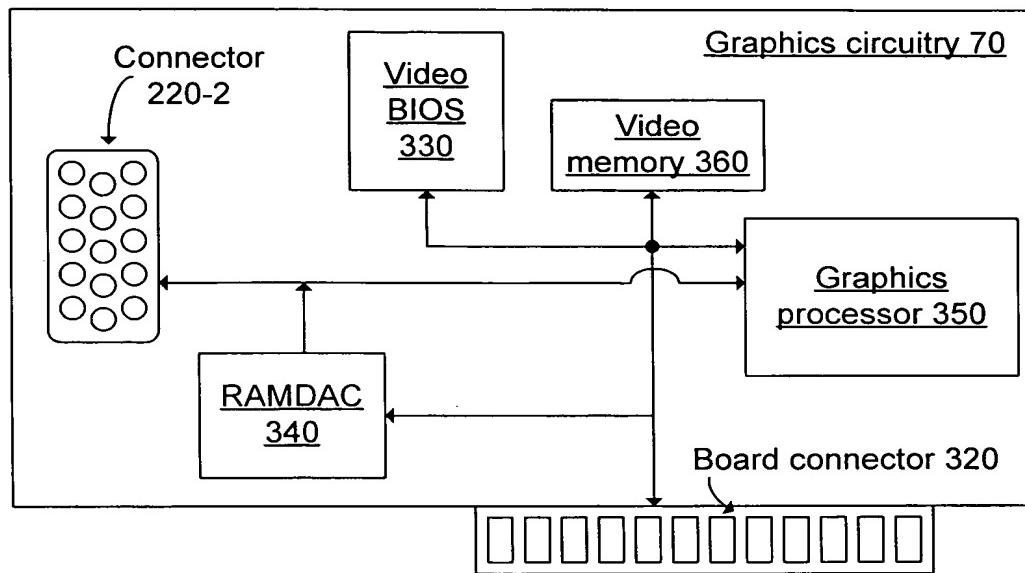


Figure 3

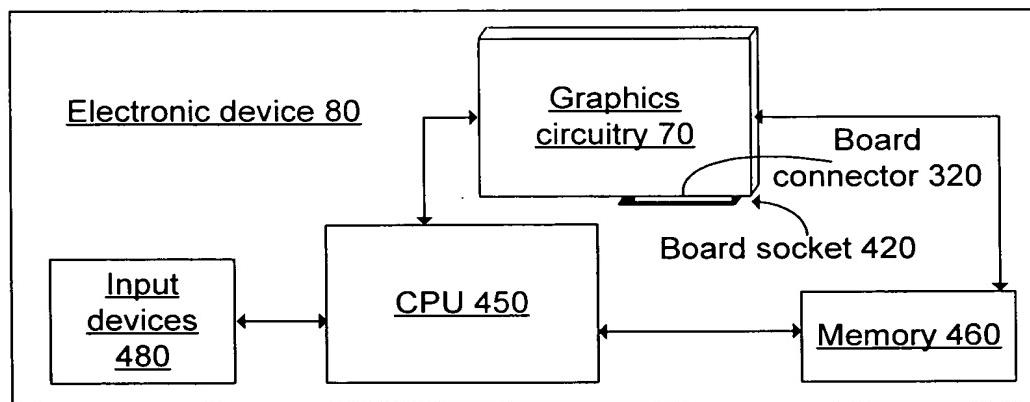
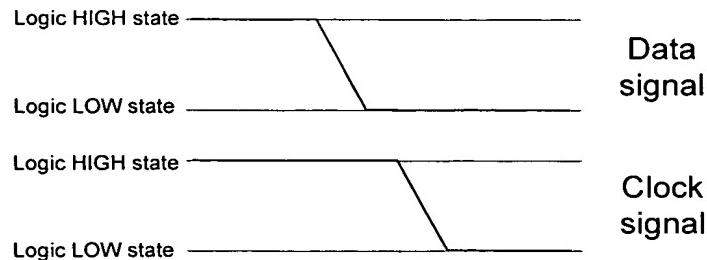
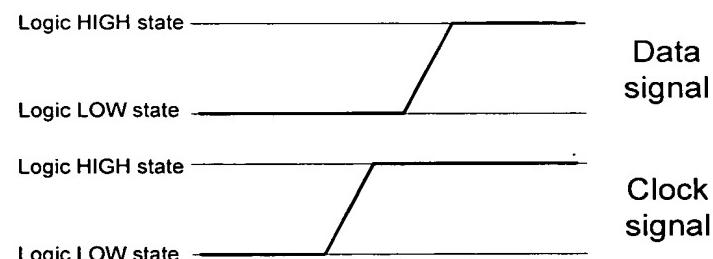
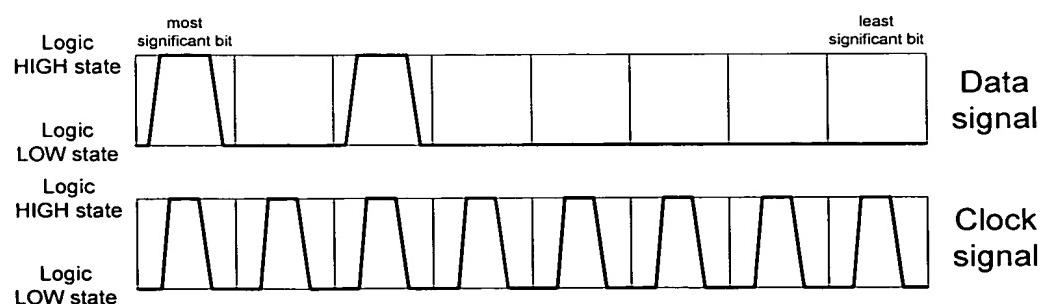
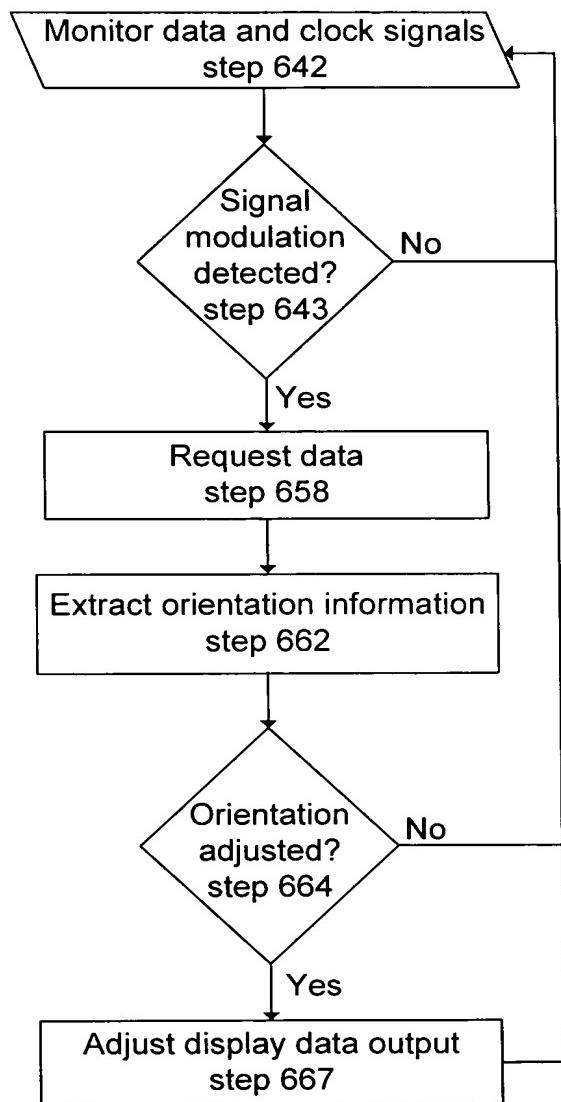
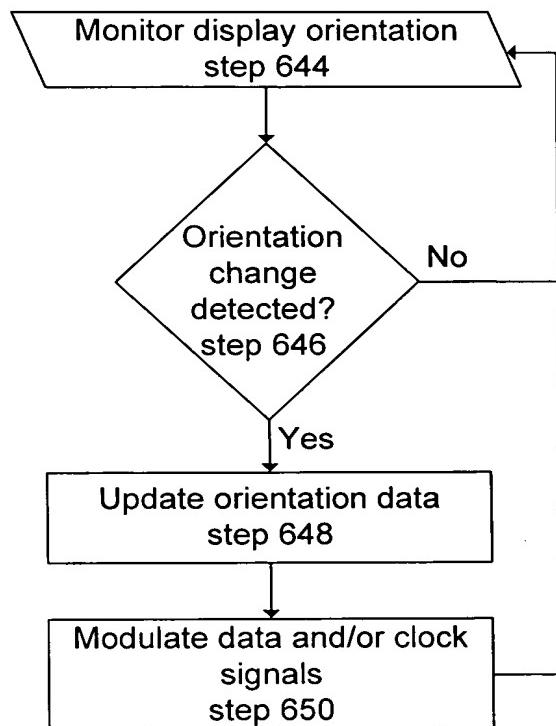


Figure 4

**Figure 5A****Figure 5B****Figure 5C**

Graphics circuitryDisplay**Figure 6**

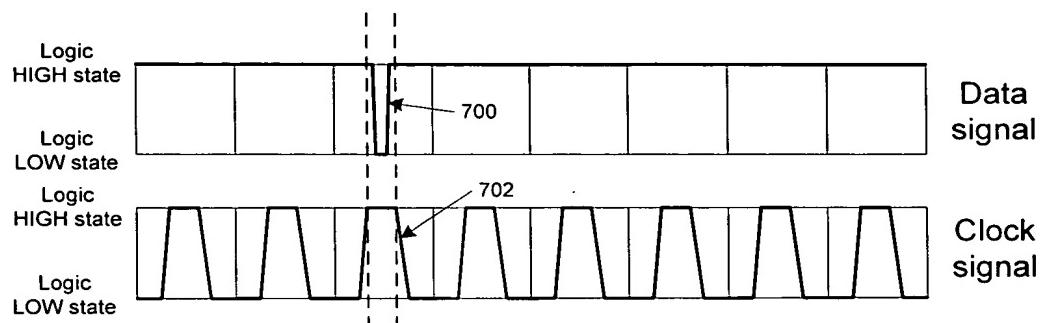


Figure 7A

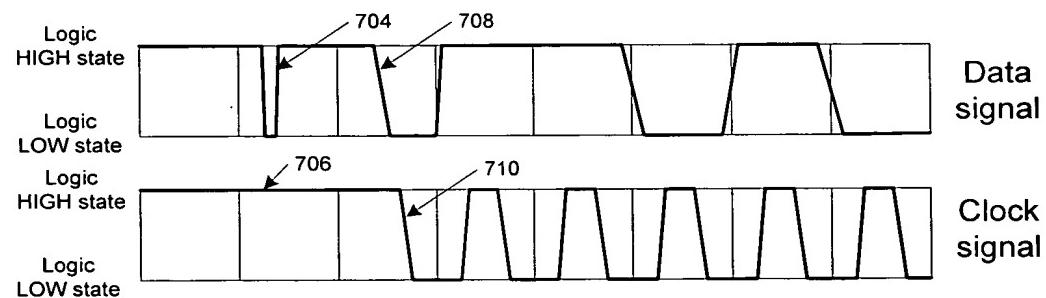


Figure 7B

